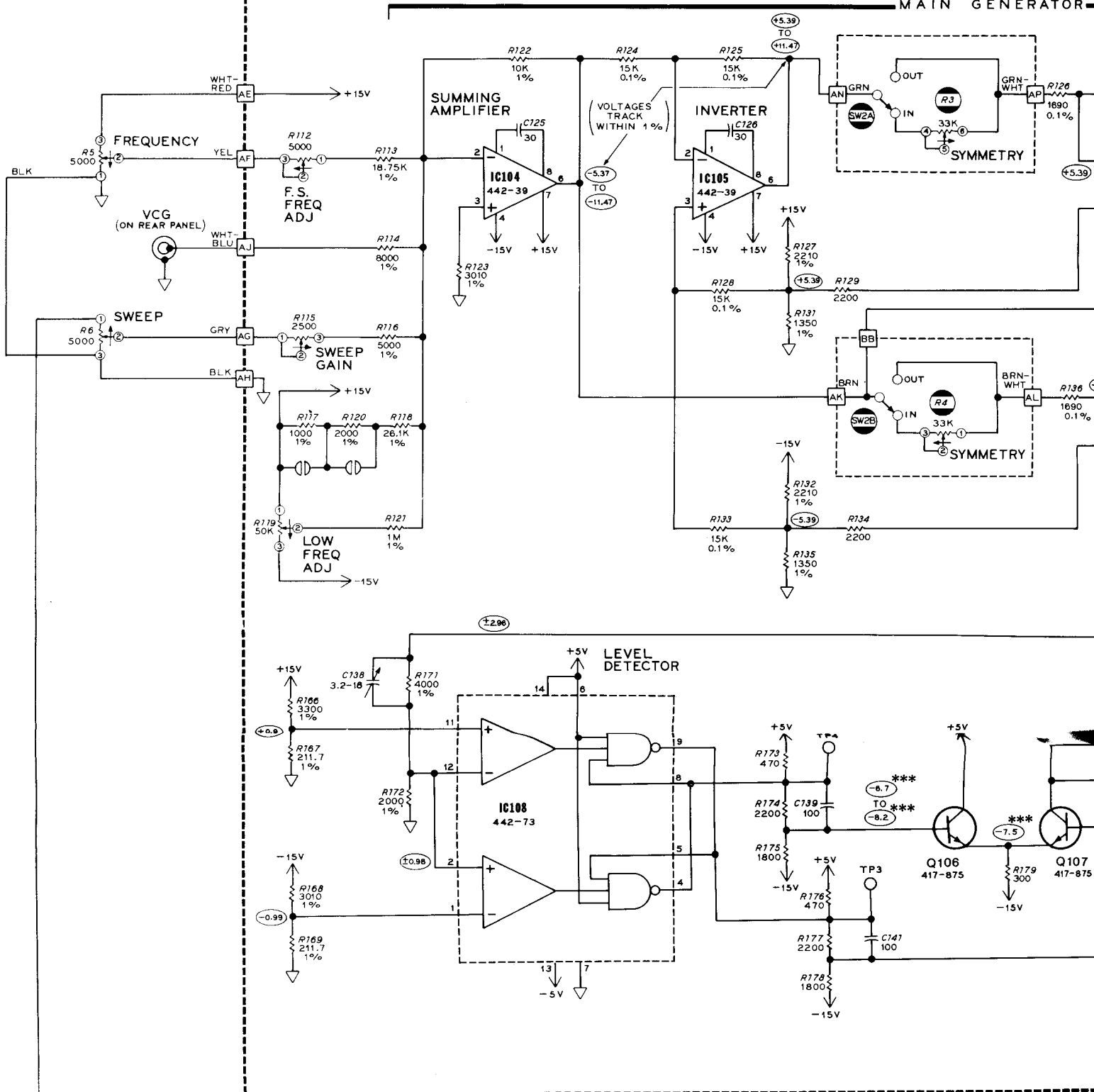
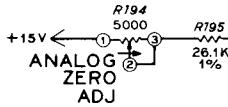
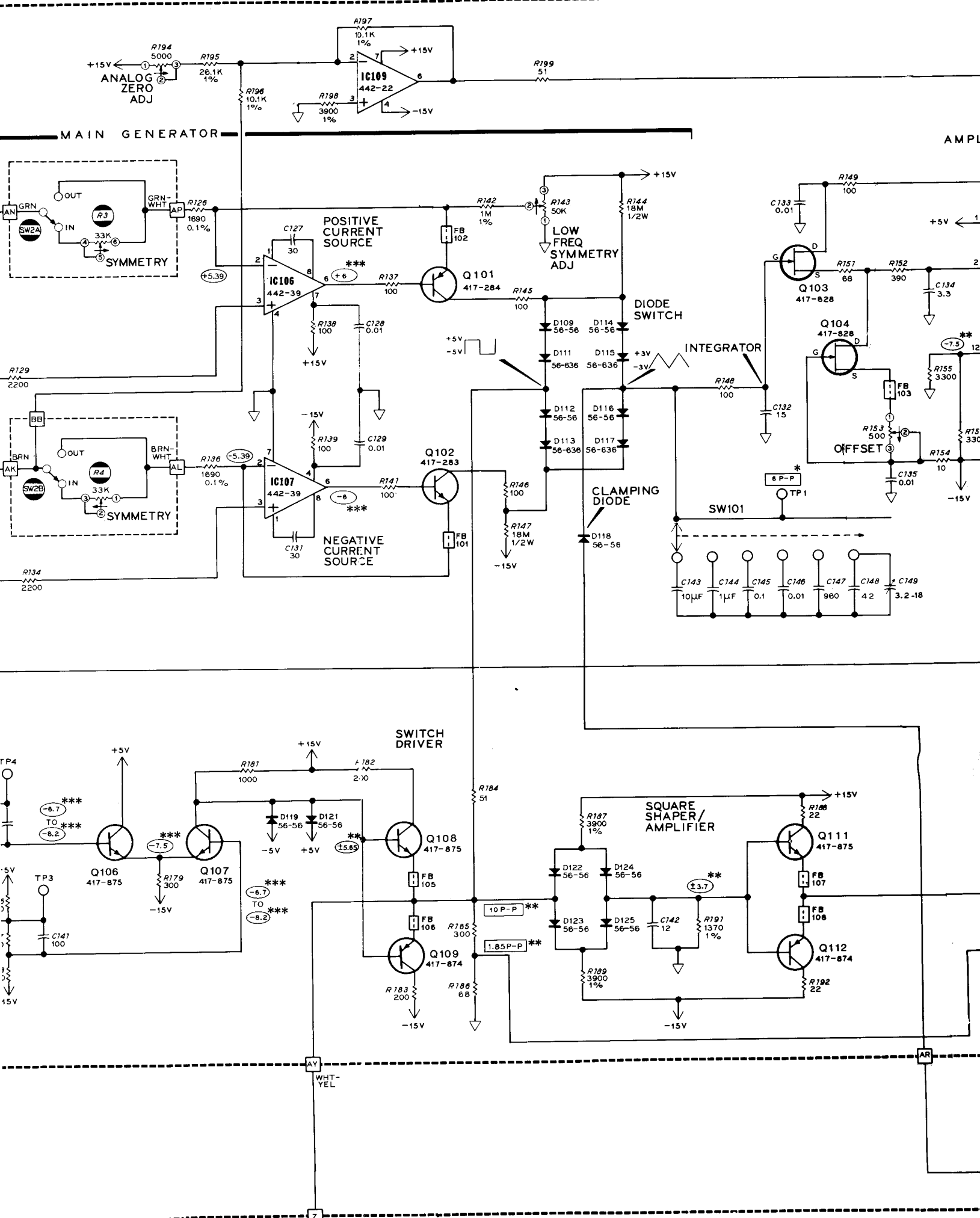
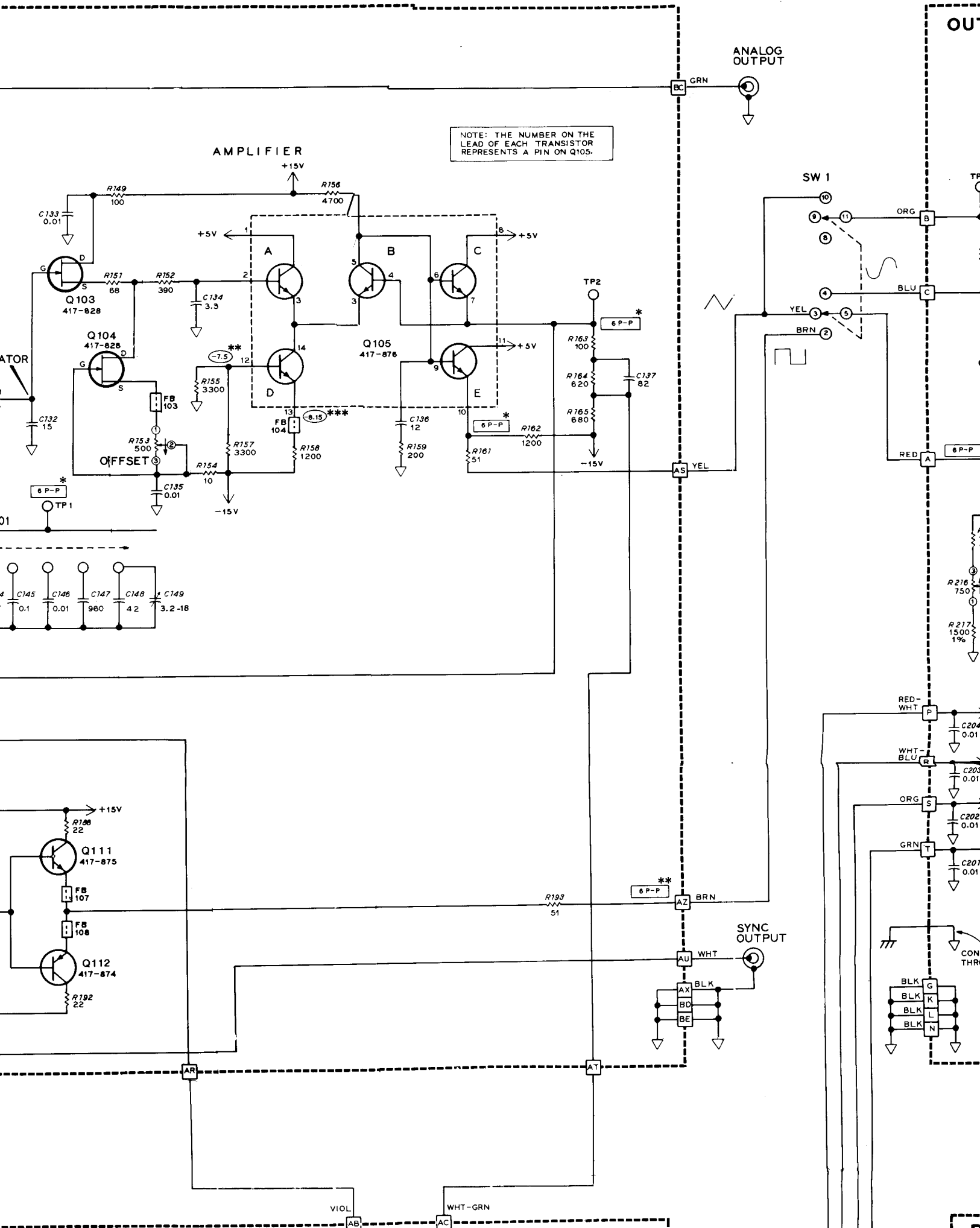


# GENERATOR CIRCUIT BOARD

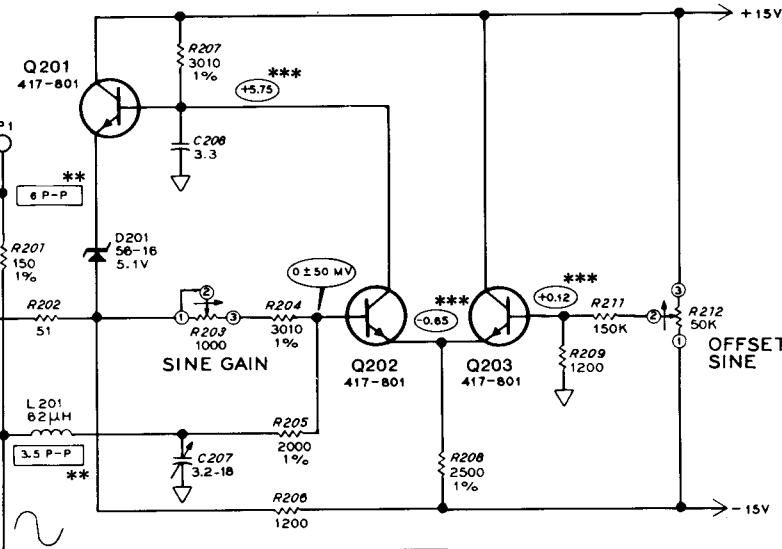




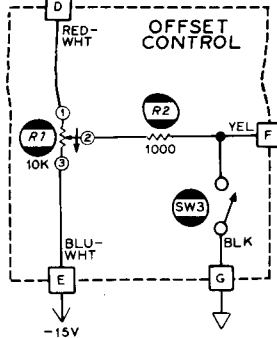
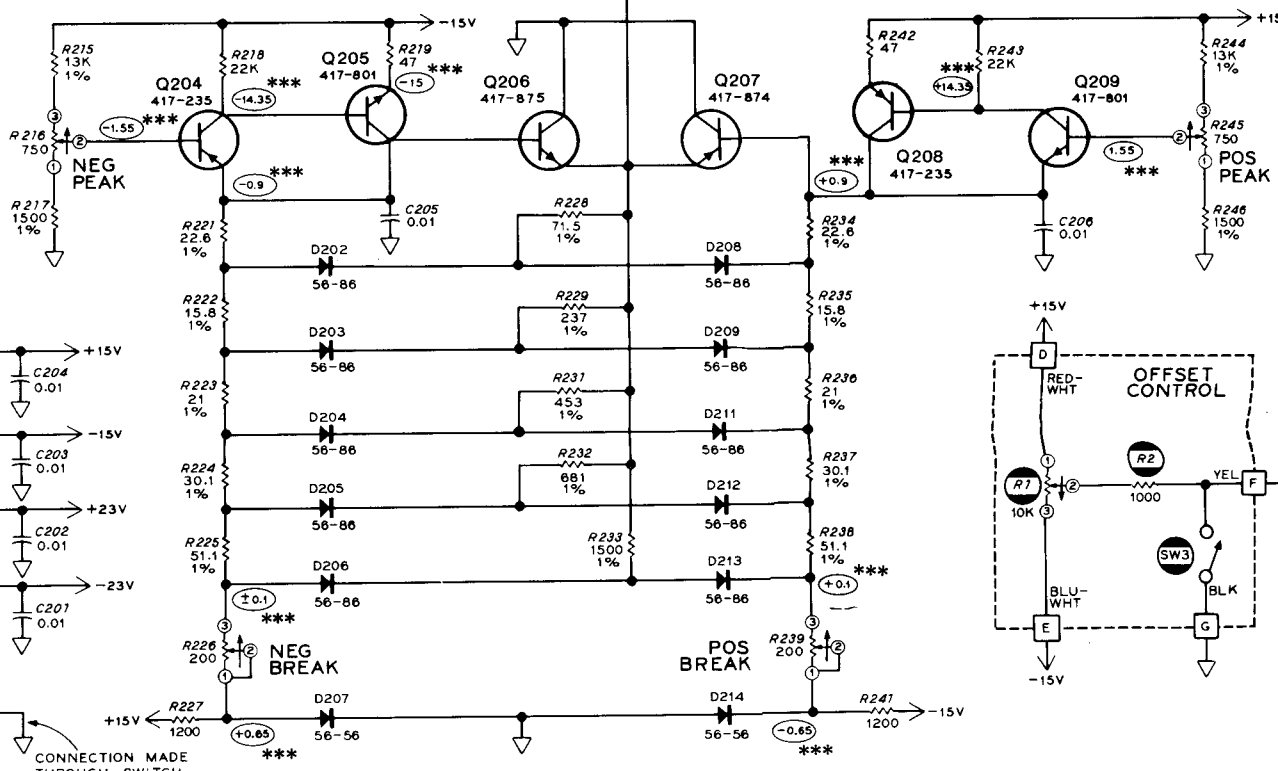


# OUTPUT CIRCUIT BOARD

## SINE WAVE AMPLIFIER



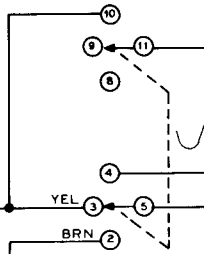
## SINE SHAPER



ANALOG OUTPUT



SW 1



RED A

RED-WHT P

WHT-BLU R

ORG S

GRN T

BLK G

BLK K

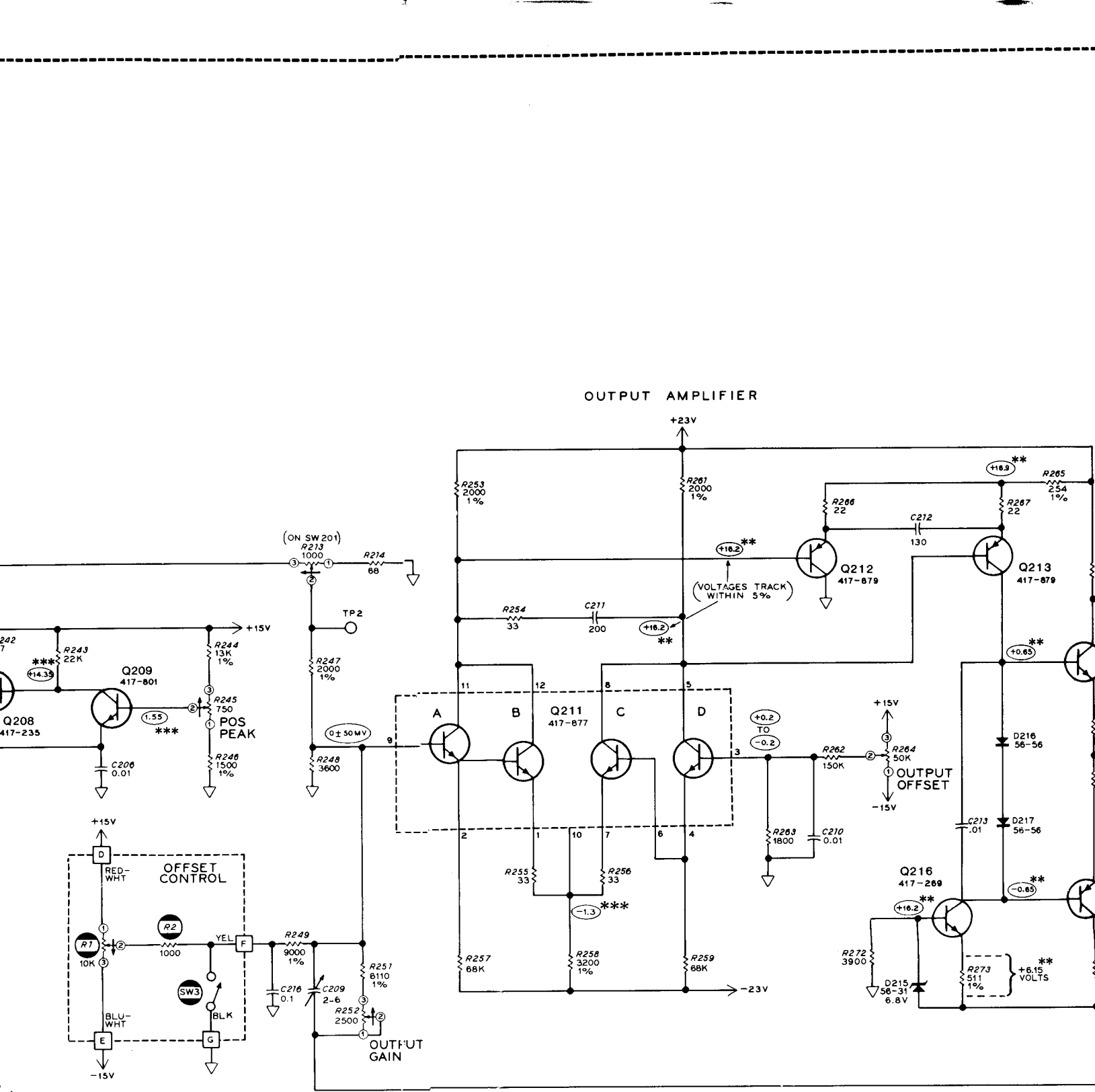
BLK L

BLK N

SYNC OUTPUT



CONNECTION MADE THROUGH SWITCH FRAME.



OUTPUT AMPLIFIER

+23V

-23V

(ON SW 201)

TP 2

OFFSET CONTROL

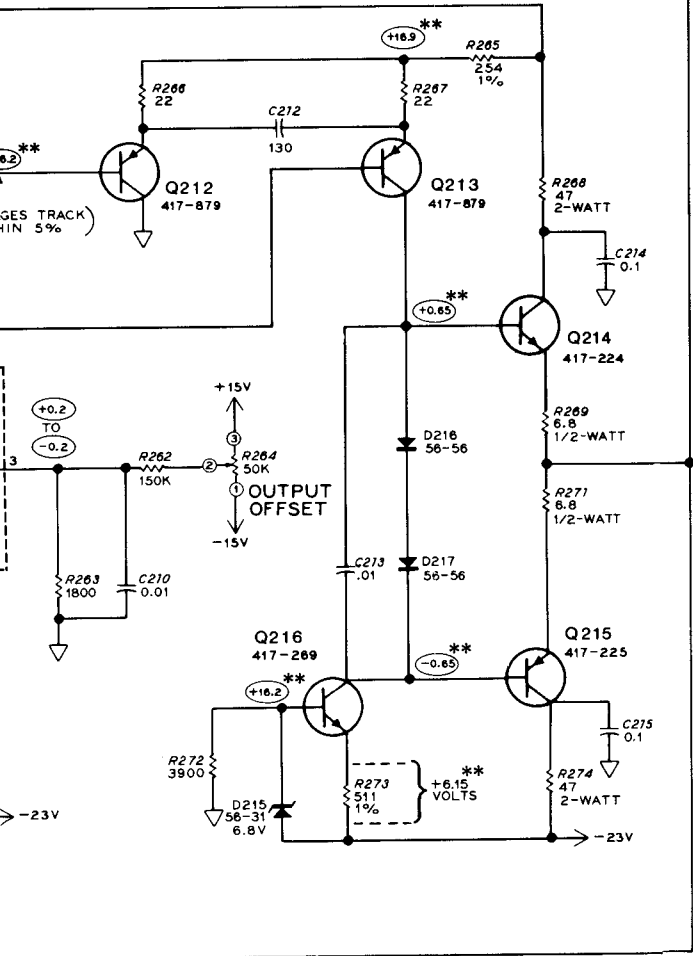
OUTPUT GAIN

(VOLTAGES TRACK WITHIN 5%)

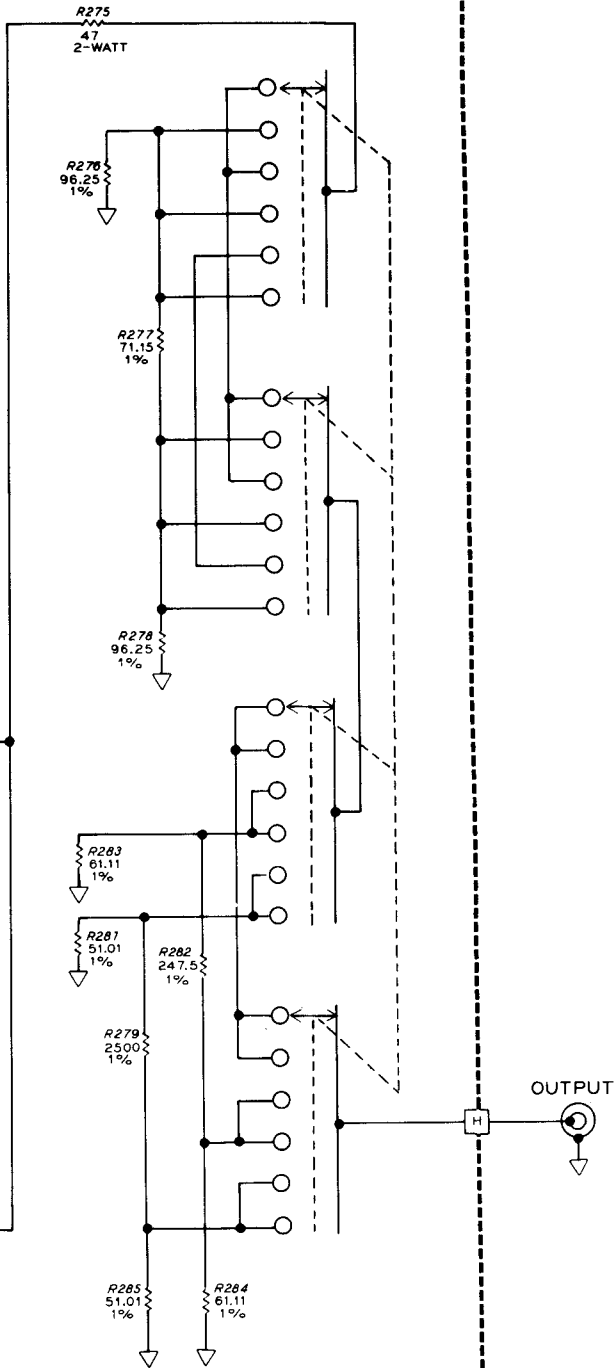
OUTPUT OFFSET

-15V

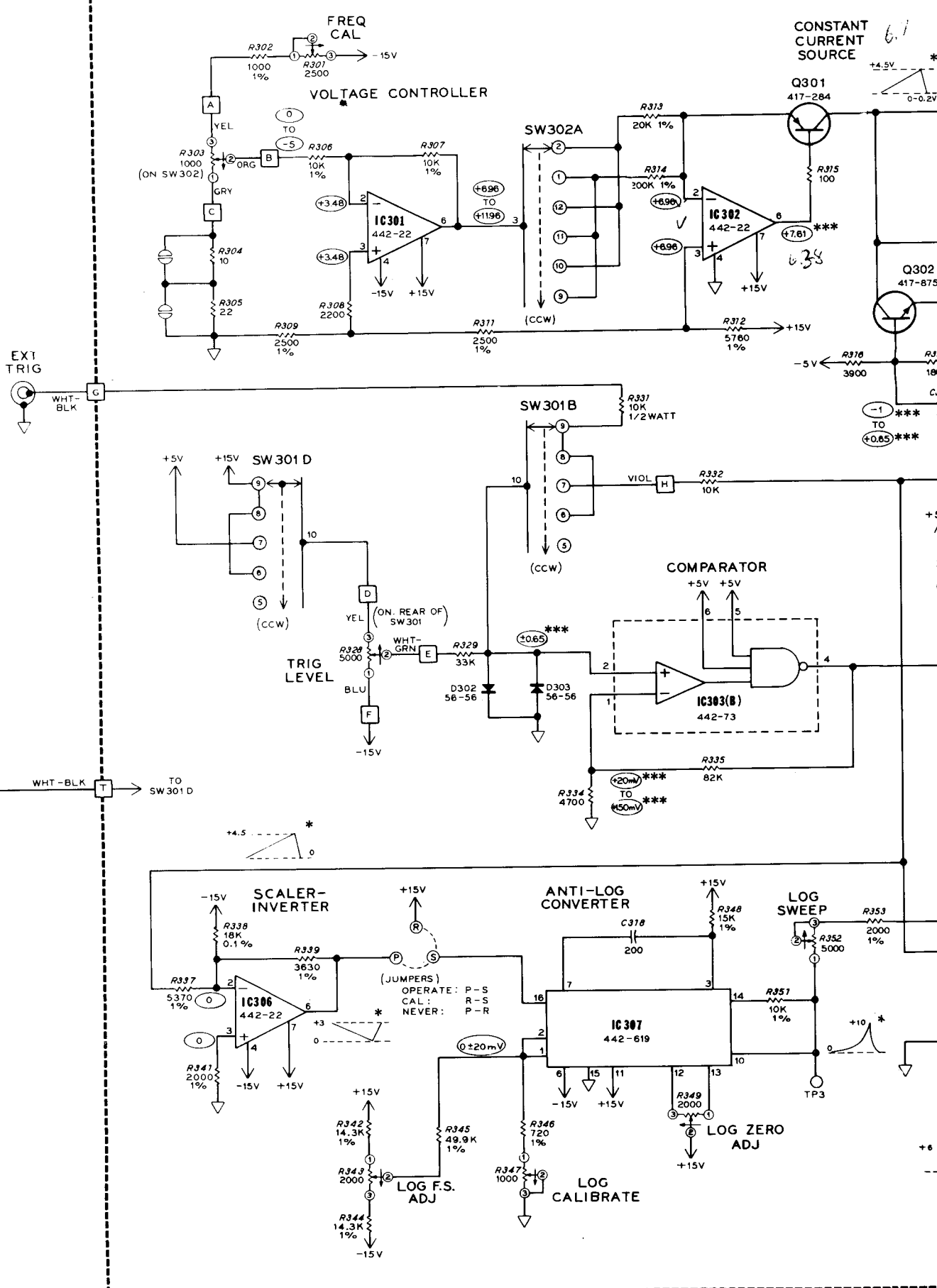
IER

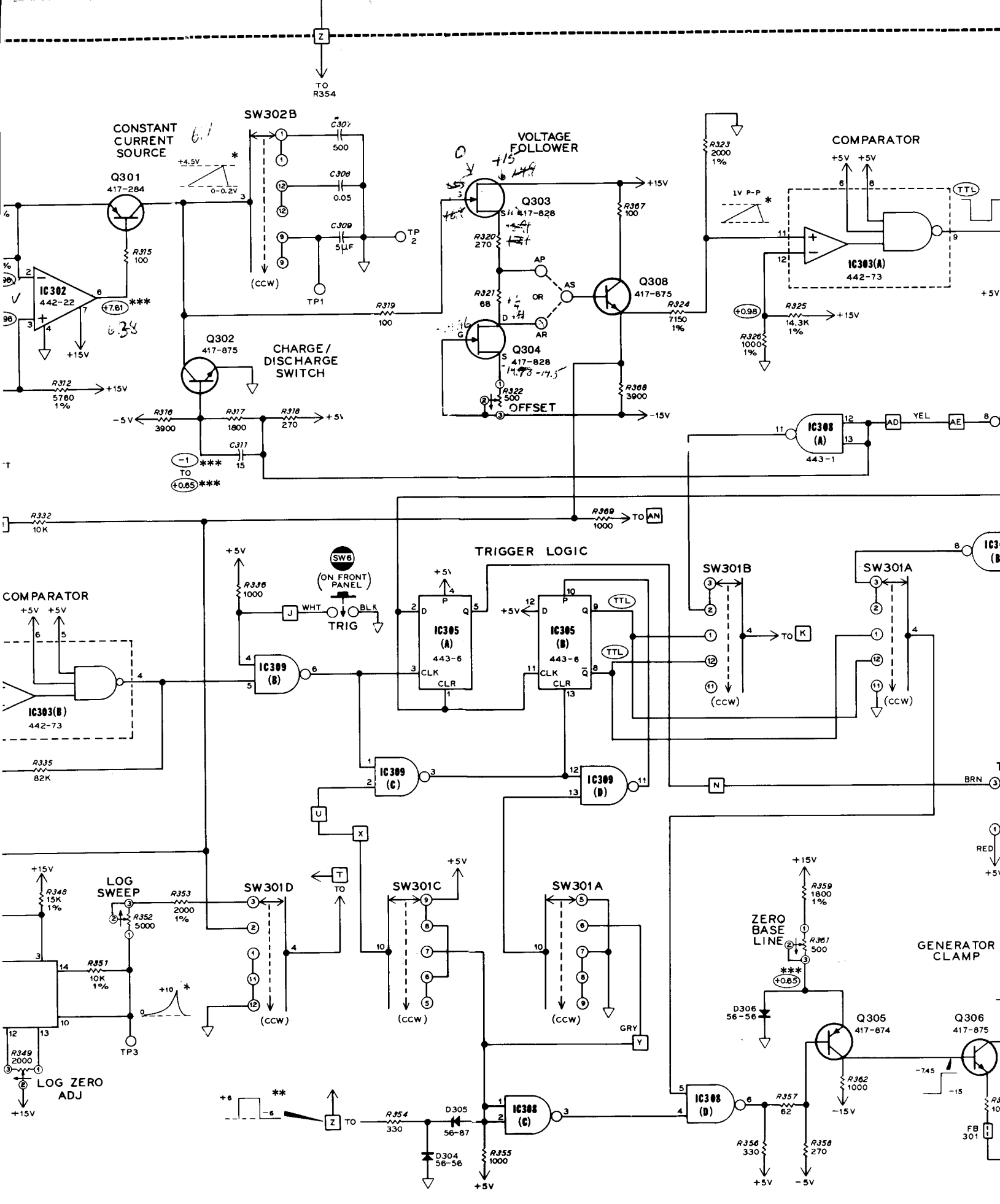


SW 201

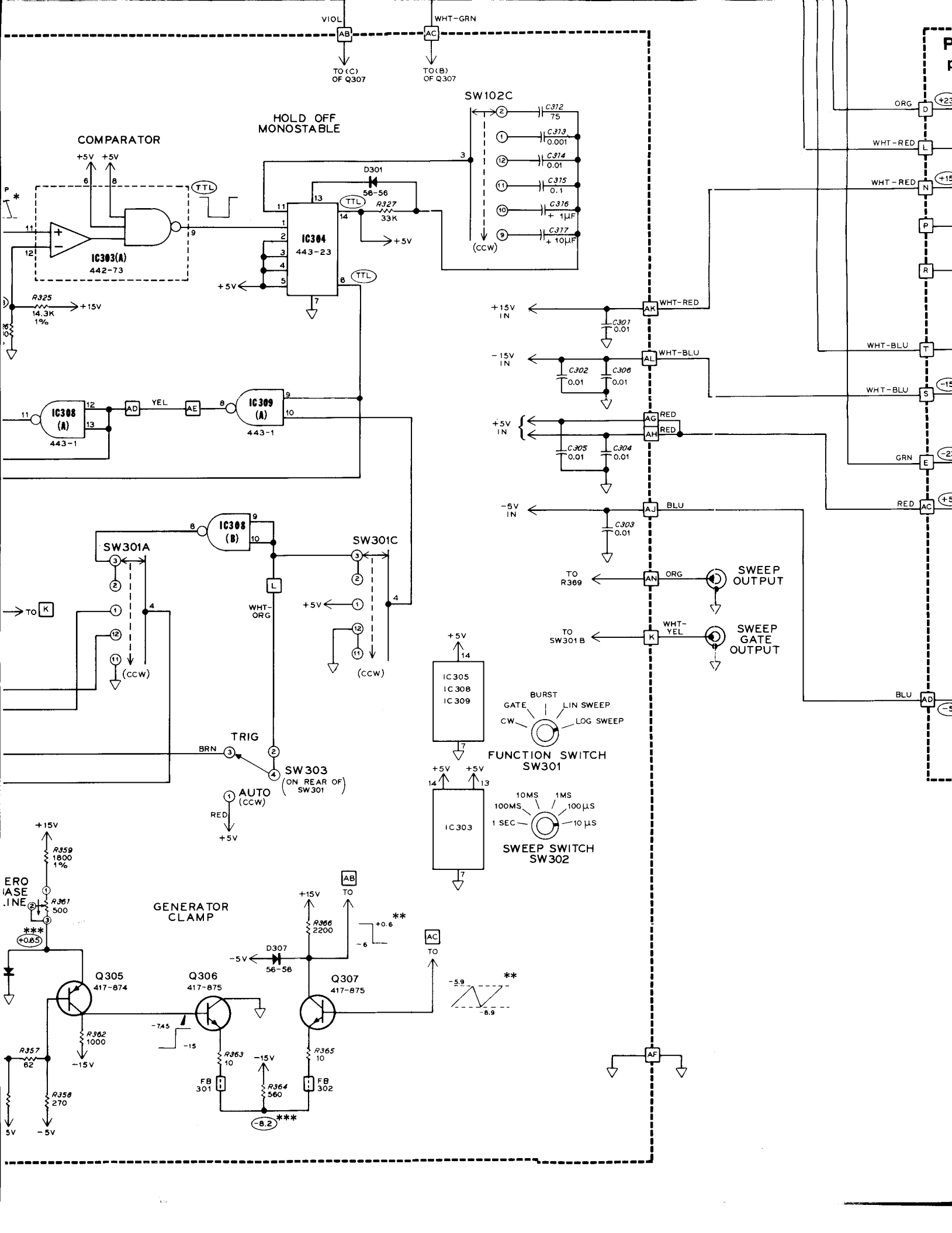


# SWEEP CIRCUIT BOARD



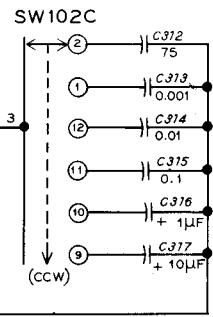




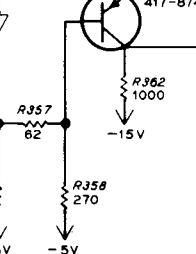
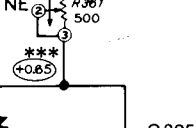
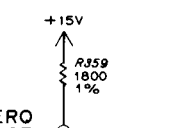
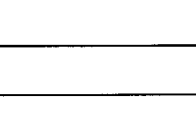
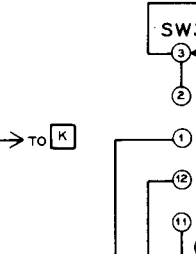
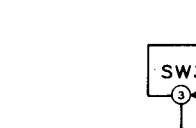
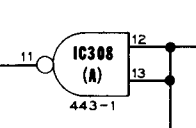
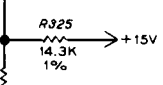
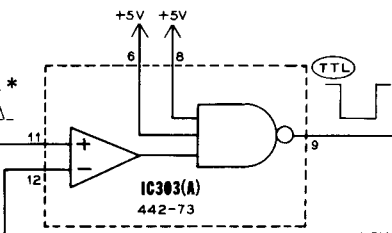


VIOL AB  
WHT-GRN AC  
TO (C) OF Q307  
TO (B) OF Q307

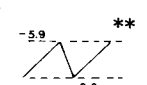
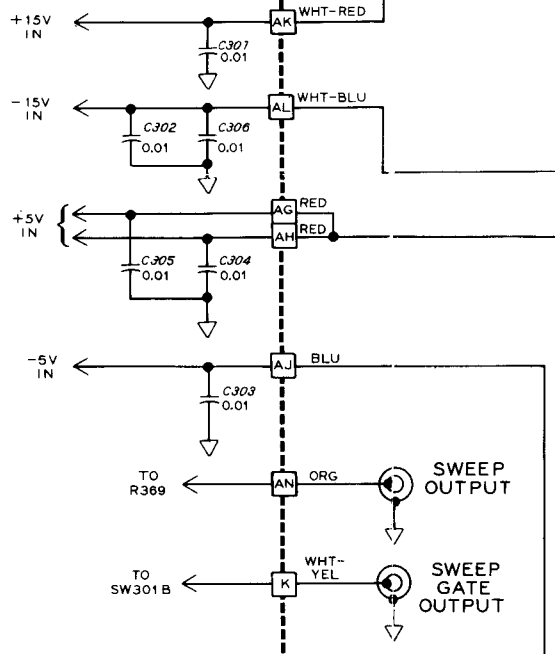
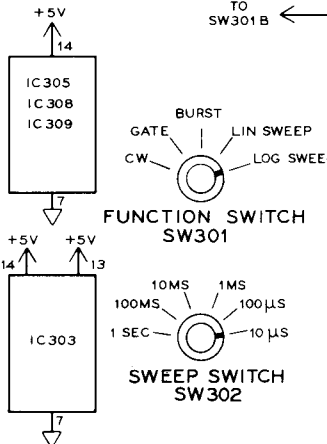
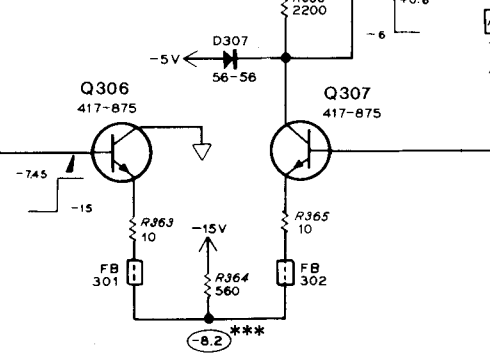
**HOLD OFF MONOSTABLE**



**COMPARATOR**

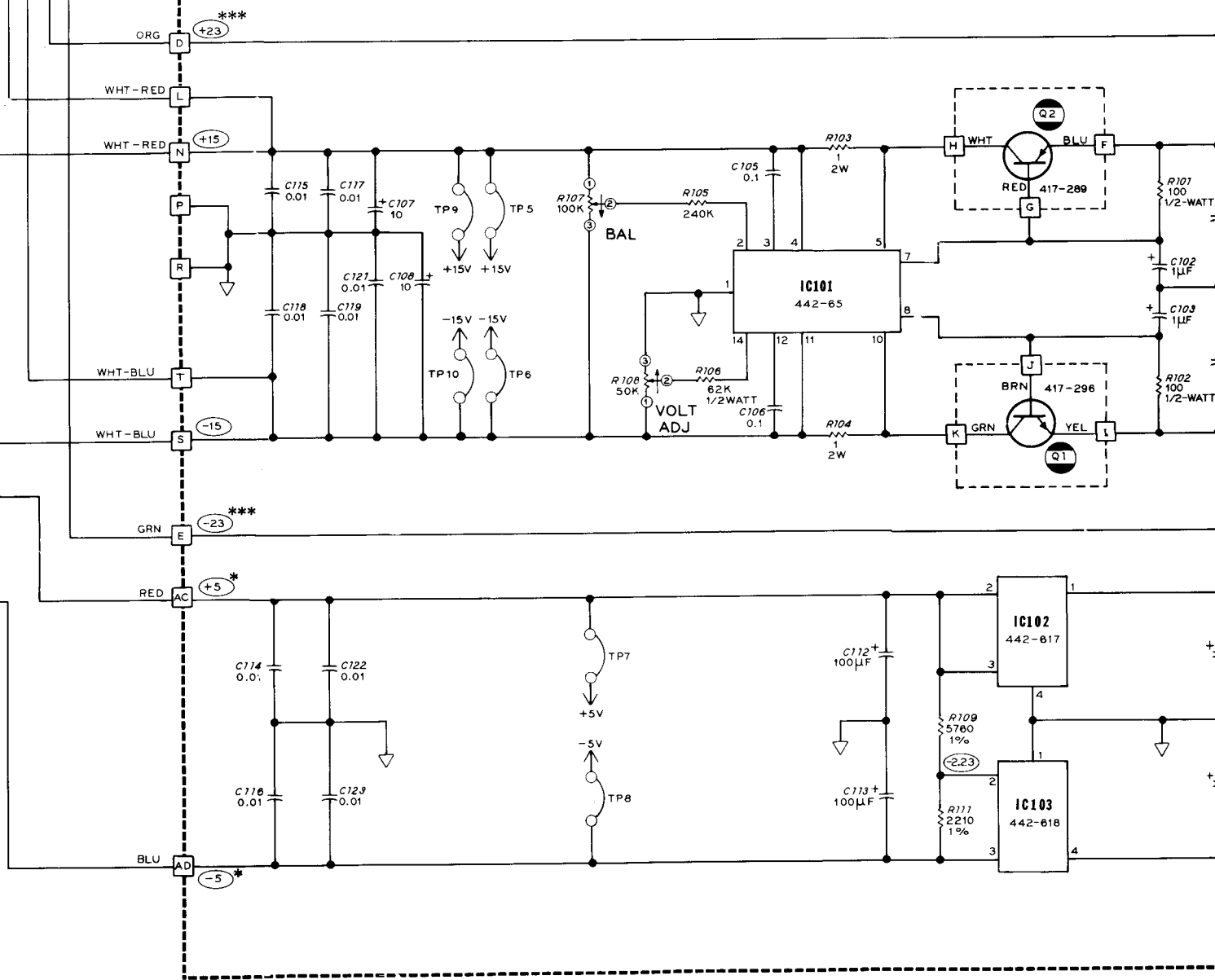


**GENERATOR CLAMP**



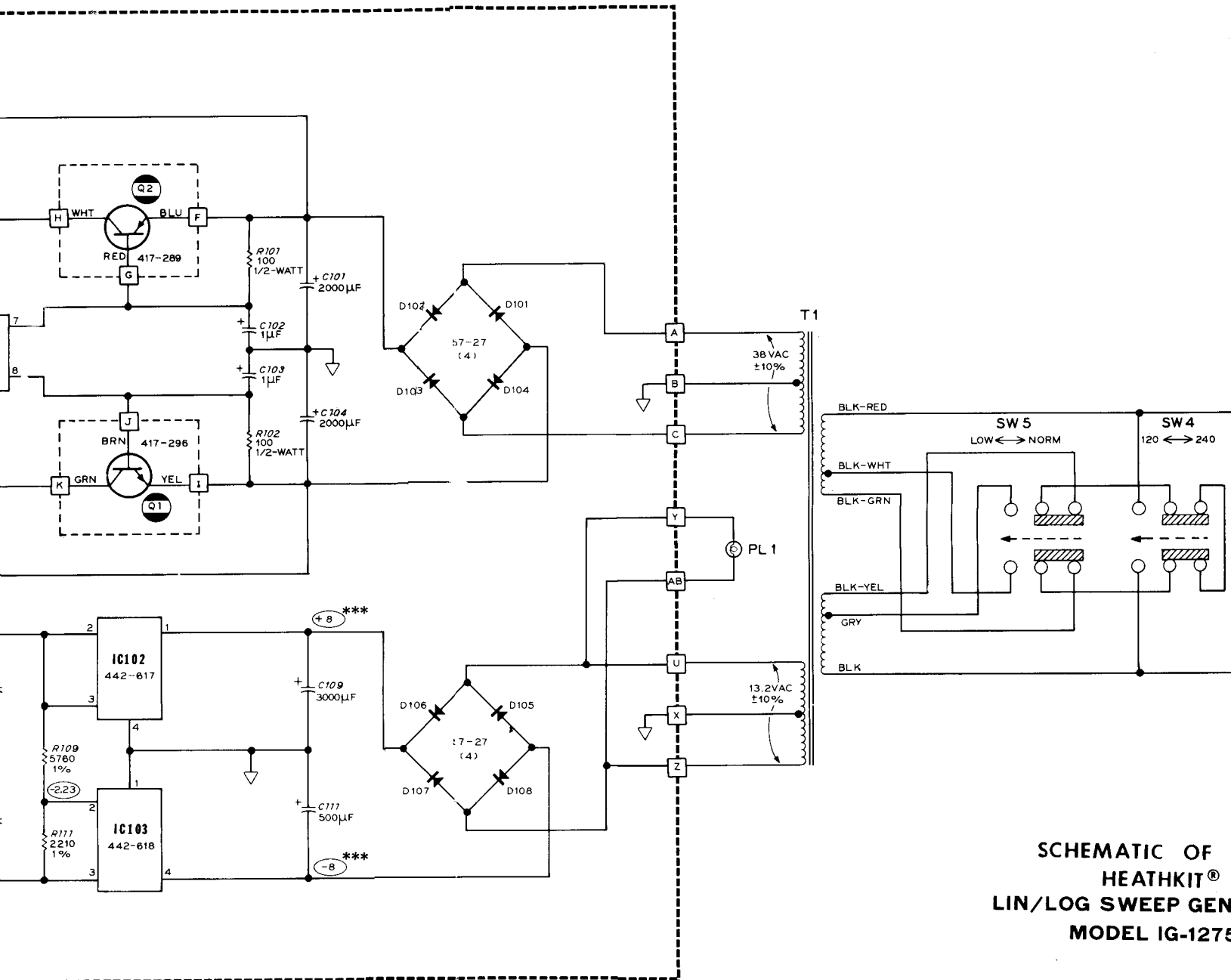
# POWER SUPPLY

## part of GENERATOR CIRCUIT BOARD



SWEEP OUTPUT





SWEEP GATE OUTPUT

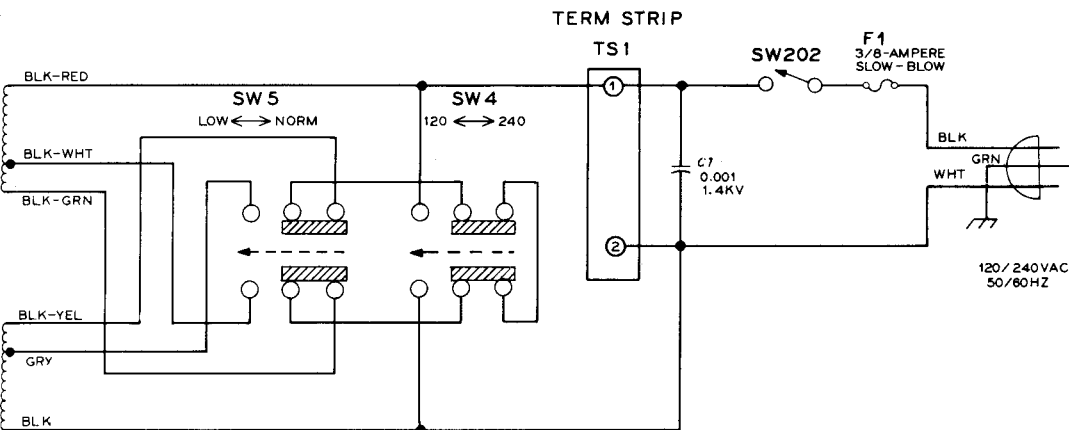


**NOTES:**

1. COMPONENTS ARE NUMBERED IN THE FOLLOWING GROUPS:

- 1- 99 MOUNTED ON CHASSIS.
- 101-199 MOUNTED ON GENERATOR CIRCUIT BOARD.
- 201-299 MOUNTED ON OUTPUT CIRCUIT BOARD.
- 301-399 MOUNTED ON SWEEP CIRCUIT BOARD.

- 2.  THIS SYMBOL INDICATES A CIRCUIT BOARD GROUND.
- 3.  THIS SYMBOL INDICATES EARTH AND CHASSIS GROUND.
- 4.  THIS SYMBOL INDICATES A CIRCUIT BOARD WIRE CONNECTION.
- 5. ALL MEASUREMENTS (VOLTAGE OR WAVEFORM) ARE SPECIFIED AT VARIOUS TOLERANCES: NO ASTERISK IS 1%, ONE ASTERISK IS 5%, TWO ASTERISKS ARE 10%, AND THREE ASTERISKS ARE 20%.
- 6.  THIS SYMBOL INDICATES A DC VOLTAGE MEASUREMENT TAKEN WITH A HIGH-IMPEDANCE INPUT VOLTMETER FROM THE POINT INDICATED TO CIRCUIT GROUND.



**SCHEMATIC OF THE  
HEATHKIT®  
LIN/LOG SWEEP GENERATOR  
MODEL IG-1275**

Part of 595-1758-01

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


ED IN THE FOLLOWING GROUPS:

ASSIS.  
ERATOR CIRCUIT BOARD.  
PUT CIRCUIT BOARD.  
EEP CIRCUIT BOARD.

ICATES A CIRCUIT BOARD GROUND.  
ICATES EARTH AND CHASSIS GROUND.  
ICATES A CIRCUIT BOARD WIRE

AGE OR WAVEFORM) ARE SPECIFIED AT  
D ASTERISK IS 1%, ONE ASTERISK IS  
0%, AND THREE ASTERISKS ARE 20%.

ICATES A DC VOLTAGE MEASUREMENT  
H-IMPEDANCE INPUT VOLTMETER FROM  
TED TO CIRCUIT GROUND.

7.  THIS SYMBOL INDICATES AN AC VOLTAGE MEASUREMENT WITH A HIGH-IMPEDANCE INPUT VOLTMETER OR CALIBRATED OSCILLOSCOPE FROM THE POINT INDICATED TO CIRCUIT GROUND.
8.  THIS SYMBOL INDICATES A CHASSIS-MOUNTED COMPONENT SHOWN WITHIN A CIRCUIT BOARD OUTLINE.
9.  ARROW INDICATES CLOCKWISE CONTROL ROTATION.
10. RESISTOR VALUES ARE IN OHMS (K=1000, M=1,000,000).
11. CAPACITOR VALUES LESS THAN 1 ARE IN  $\mu$ F (MICROFARADS). ALL OTHER CAPACITORS ARE IN pF (PICOFARADS) UNLESS MARKED OTHERWISE.
12. REFER TO THE CIRCUIT BOARD X-RAY VIEWS FOR THE PHYSICAL LOCATION OF PARTS.